

William Paterson University  
College of Science and Health - Department of Computer Science

Fall 2013 – Spring 2015 Assessment Cycle  
Analysis of the Course Coverage and Assessment Report Data

Course Number: CS3410

Course Coordination Committee Members: John Najarian, Bogong Su (chair)

Date: June 26, 2015

**A. Course Prerequisites/Co-requisites**

a) Problems/Issues Identified: None

b) Suggestions for Improvement: N/A

**B. Course Objectives**

a) Problems/Issues Identified: None

b) Suggestions for Improvement: N/A

**C. Course Student Learning Outcomes**

a) Problems/Issues Identified: None

b) Suggestions for Improvement: N/A

**D. Course Content**

a) Problems/Issues Identified: None

b) Suggestions for Improvement: N/A

## **E. Assessment of the CS Program's Student Outcomes**

### **Student Outcome S1: Effectively communicate in written and oral forms.**

In this course students complete a digital circuit project that includes its design, implementation and simulation in a team of two students. They also produce a report of their project and also make an oral presentation. The report is evaluated by the instructor of the course based on its style and presentation whereas the presentation is evaluated by the whole class, including the instructor. This student outcome is therefore OK for this course.

### **Student Outcome S4: Work effectively as part of a team in a software or hardware project.**

In this course students complete a digital circuit project that includes its design, implementation and simulation in a team of two students. They also produce a report of their project and also make an oral presentation. The report is evaluated by the instructor of the course based on its style and presentation whereas the presentation is evaluated by the whole class, including the instructor. One criterion in the evaluation of students' work is how well they work together on the project.

### **Student Outcome S10:**

#### **Demonstrate competence in computer organization and architecture..**

In this three-credit course, nearly the entire semester is spent on Boolean algebra, Boolean function minimization, combinational and sequential circuit design. As a result, there is little time to teach computer organization at the end of semester. Usually I give a brief introduction to computer organization and architecture, but has no time to give homework, its contents are not covered in final exam.